

Docket No.: 049657-0875

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Response Under 37 CFR 1.116 - Expedited Procedure

: Customer Number: 20277

Tetsushi TANIZAKI, et al.

: Confirmation Number: 8725

Application No.: 09/712,246

: Group Art Unit: 2133

Filed: November 15, 2000

: Examiner: M.M. Chaudry

For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT OPERATING AT HIGH RATE

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.
☐ Applicant is entitled to small entity status under 37 CFR 1.27
☐ Also attached:

The fee has been calculated as shown below:

| | NO. OF CLAIMS | HIGHEST PREVIOUSLY PAID FOR | EXTRA CLAIMS | RATE | FEE |
|---|------------------|-----------------------------------|-----------------|------------|--------|
| Total Claims | 4 | 20 | 0 | \$50.00 = | \$0.00 |
| Independent Claims | 2 | 3 | 0 | \$200.00 = | \$0.00 |
| Multiple dependent claims newly presented | | | | | \$0.00 |
| Fee for extension of time | | | | | \$0.00 |
| | | | | | \$0.00 |
| Total of Above Calculations | | | | | \$0.00 |

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

Tomoki Tanida

Tomoki Tanida
Recognition Under 37 CFR 10.9(b)

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Date: April 7, 2005

Please recognize our Customer No. 20277 as our
correspondence address.

Corres. and Mail
BOX AF

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Applicant: Tetsushi TANIZAKI, et al.

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE

Application No.: 09/712,246

Customer No.: 20277

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Group Art Unit: 2133

Examiner: M.M. Chaudry

Title: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT
OPERATING AT HIGH RATE

AMENDMENT UNDER 37 CFR 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following Remarks are submitted in response to the Office Action dated January 27, 2005, pursuant to the provisions of 37 C.F.R. § 1.116.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.